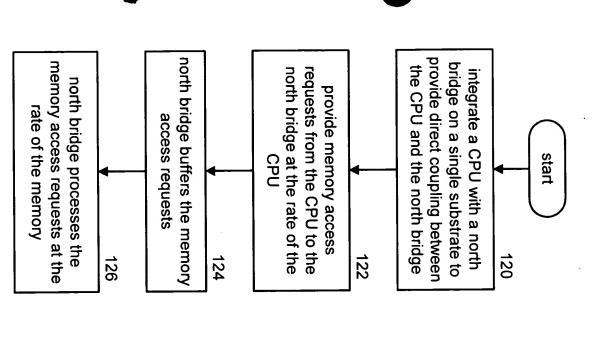


FIG. 3

100

DOPYTOY: LEEDO



memory access requests at the north bridge buffers the memory south bridge to the north bridge requests from the CPU and the bridge and a south bridge on a integrate a CPU with a north north bridge processes the provide memory access at the rate of the CPU rate of the memory access requests single substrate start 130 136 134 132

DOUTZIBZZ INDEGO

FIG. 5

FIG. 6